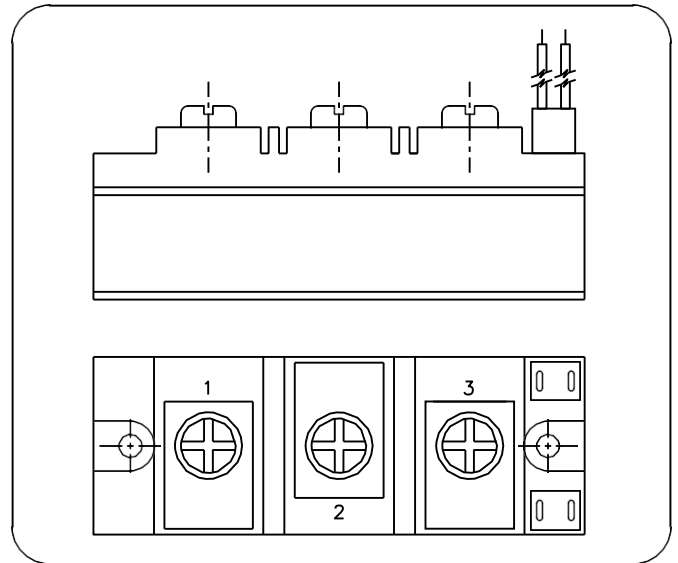


POWER MODULES
IRK. 162 Series
Dual Thyristor Module 1600V, 162A 150mA
SKU: A-D45027

FEATURES

- # Electrically isolated base plate.
- # 3000Vrms isolating voltage.
- # Industrial standard package.
- # Simplified mechanical designs, rapid assembly.
- # High surge capability.
- # Large creepage distance.
- # Aluminum Nitride.



DESCRIPTION

These IRK series of Power Modules use power diodes and thyristor in four basic configuration. The semiconductors are electrically isolated from the metal base, allowing common heat sinks and compact assemblies to be built. They can be interconnected to form single phase or three phase bridges or AC controller. These modules are connected in anti-parallel

These module are intended for general purpose applications such as battery chargers.welders and plating equipment.

MAJOR RATING & CHARACTERISTICS

Parameters	IRK. 162	Units
$I_{T(AV)}$ @Tc-85°C	160	A
$I_{T(RMS)}$	355	A
I_{TSM} @ 50Hz	5100	A
I^2t @ 50Hz	131.0	kA ² s
V_{RRM}	400 to 1600	V
T_J	-40 to 130	°C

POWER MODULES

IRK. 162 Series

ELECTRICAL SPECIFICATION

VOLTAGE RATINGS

Type Number	Voltage Code	V_{RRM} max. repetitive peak reverse and off-state blocking voltage V	V_{RSM} max.non-repetitive peak reverse voltage V	I_{DRM} / I_{RRM} max. @ 130°C Max. mA
IRK.162	04	400	500	50
	06	600	700	
	08	800	900	
	10	1000	1100	
	12	1200	1300	
	14	1400	1500	
	16	1600	1700	

ON-STATE CONDUCTION

	Parameter	IRK. 162	Unit	Conditions	
I _{T(AV)}	Max, average On-state current @ case temperature	160 85	A °C	180°C conduction, half sine wave	
I _{T(RMS)}	Max, RMS on-state current	355	A	as AC switch	
I _{TSM}	Max, peak, one cycle on-state, non-repetitive surge current	5100	A	t = 10ms	Sinusoidal half wave initial T _j = T _j max.
I ² t	Maximum I ² t for fusing	131.0	kA ² s	t = 10ms	
V _{T(TO)}	Max, value of Threshold voltage	0.88	V	T _j = T _j max.	
r _t	Max, value of on-state slope resistance	1.20	mΩ	T _j = T _j max,	
V _{TM}	Max, on-state voltage drop	1.50	V	= π x I _{T(AV)} , T _j = T _j Max, 180° conduction AV Power = V _{T(TO)} x I _{T(AV)} + r ₁ x (I _{T(RMS)}) ²	
I _H	Maximum holding current	500	mA	Anode supply = 12V initial I _T = 30A, T _J = 25°C	
I _L	Max, latching current	300	mA	Anode supply = 12V resistive Load = 1Ω, gate pulse. 10V, 100μs, T _j = 25°C	

SWITCHING

t _d	Typical delay time	1.0	μs	T _j = 25°C	Gate current = 1A dig/dt = 1A/μS V _d = 0.67% V _{DRM}
T _R	Typical rise time	2.0	μs	T _j = 25°C	
r _q	Typical turn-off time	50-150	μs	I _{TM} = 300A, di/dt = 15A/μs, T _j = T _j max, V _r = 50V, dv/dt = 20/μs. Gate 0V, 100Ω	

POWER MODULES

IRK. 162 Series

BLOCKING

	Parameter	IRK. 162	Unit	Conditions
dv/dt	Max, critical rate of rise of off-state voltage	1000	V/ μ s	T _j = 125°C exponential to 0.67% rated V _{DRM} .
I _{RRM} I _{DRM}	Max. peak reverse and off state leakage current at V _{RRM} V _{DRM}	50	mA	T _j = 125°C, rated V _{DRM} /V _{RRM} Applied
V _{INS}	RMS Isolation voltage	3000	V	50Hz, Circuit to base, all terminal shorted, 25°C t=1 sec.

ELECTRICAL SPECIFICATION TRIGGERING

	Parameter	IRK. 162	Unit	Conditions
P _{GM}	Max, peak gate power	10	W	T _j = 125°C, t _p ≤ 5ms
P _{G(AV)}	Maximum average gate power	2.0		T _j = 125°C, f = 50Hz, d% = 50
I _{GM}	Max, peak positive gate current	3.0	A	T _j = 125°C, t _p ≤ 5ms
+V _{GM}	Max. peak positive gate voltage	20	V	T _j = 125°C, t _p ≤ 5ms
-V _{GM}	Max. peak negative gate voltage	5.0	V	
I _{GT}	DC gate current required to trigger	200	mA	T _j = 25°C Max required gate trigger/current/voltage are the lowest value which will trigger all units 12V anode-to-cathode applied.
V _{GT}	DC gate voltage required to trigger	3.0	V	
V _{GD}	DC gate voltage not to trigger	0.30	V	T _j = 125°C Max required gate trigger/current/voltage are the lowest value which will trigger all units 12V anode-to-cathode applied.
I _{GD}	DC gate current not to trigger	10	mA	
dv/dt	Max, critical rate of rise of turn-on current	500	A/ μ s	T _j = 125°C I _{TM} = 400A, rated V _{DRM} applied.

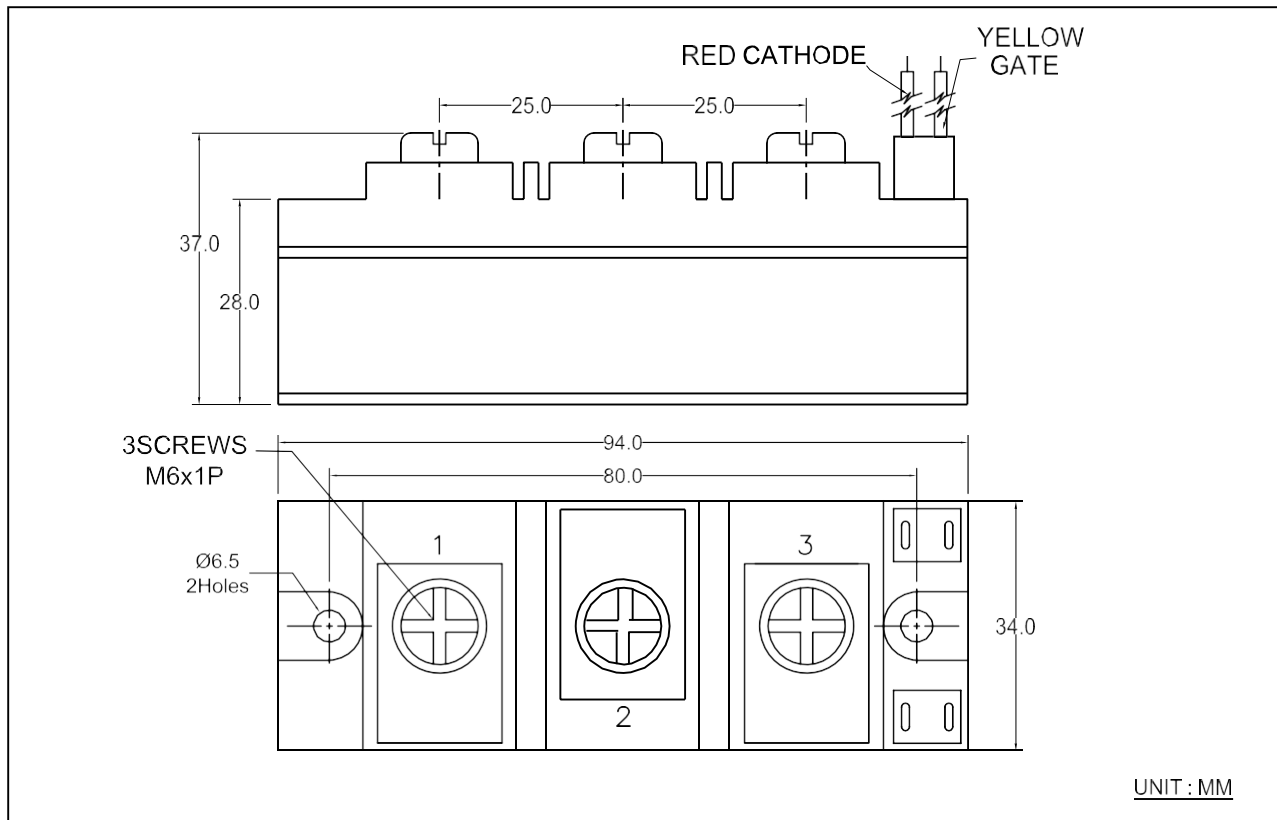
THERMAL AND MECHANICAL SPECIFICATION

	Parameter	IRK. 162	Unit	Conditions
T _j	Junction operating temperature	-40 to 130	°C	
T _{stg}	Max, storage temperature range	-40 to 150		
R _{thj-c}	Max. thermal resistance, junction to case	0.17	K/W	Per Junction, DC operation
R _{thc-h}	Max. thermal resistance junction to heatsink	0.035	K/W	Mounting surface flat smooth and greased (per Module)
T	Mounting torque ±10%	4 to 6	Nm	For module to heat sink and bus bar to module
Wt	Approximate Weight	500	g	
	Case style	INT-A-PAK		

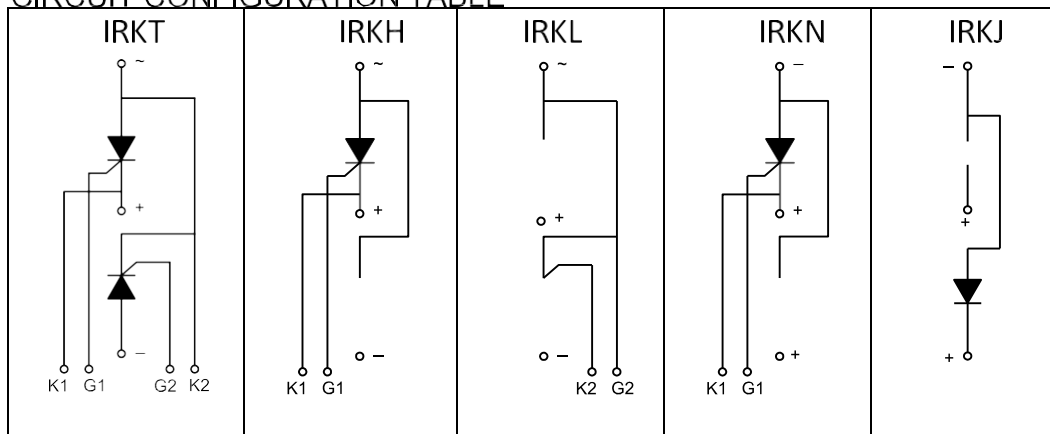
POWER MODULES

IRK. 162 Series

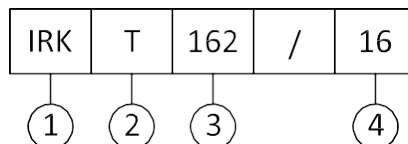
OUTLINE DIAGRAM



CIRCUIT CONFIGURATION TABLE



CIRCUIT CONFIGURATION TABLE



- 1). - Module Type
- 2). - Circuit configuration (See Circuit Configuration table)
- 3). - Current Code
- 4). - Voltage Code (See Voltage Rating Table)